

**CIRCUIT FOR PROGRAMMING A NON-VOLATILE MEMORY DEVICE
WITH ADAPTIVE PROGRAM LOAD CONTROL**

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ABSTRACT

A circuit (115,145,150), for programming a non-volatile memory device (100) having a plurality of memory cells (105), includes a plurality of driving
10 elements (115) each one for applying a program pulse to a selected memory cell to be programmed. The driving elements are suitable to be supplied by a power supply unit (120,125), and a control means (145,150) controls the driving elements (115). The control means (145,150) includes means (150,205) for determining a residual capacity of the power supply unit, and a
15 selecting means (145) selectively enables the driving elements (115) according to the residual capacity. A method of programming, an integrated circuit, and a computer system are also disclosed.